

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: Andrew Ho, Vladimir Stojanovic, Bruno W. Garlepp and Fred F. Chen  
Assignee: Rambus Inc.  
Title: "Margin Test Methods and Circuits"  
Serial No.: 10/815,604 Filed: March 31, 2004  
Examiner: Jason Perilla  
Docket No.: RA290.CIP1.US

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Filed Electronically

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Sir:

Pursuant to 37 C.F.R. §1.56, §1.97 and §1.98, Applicants brings the 40 documents listed on the enclosed form to the Examiner's attention in the above-captioned application. Citation of the listed document shall not be construed as:

1. an admission that the documents are necessarily prior art with respect to the instant application;
2. a representation that a search has been made; or
3. an admission that the information cited is, or is considered to be, material to patentability as defined in §1.56(b).

Respectfully submitted,

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09/08/2009

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /J.P./